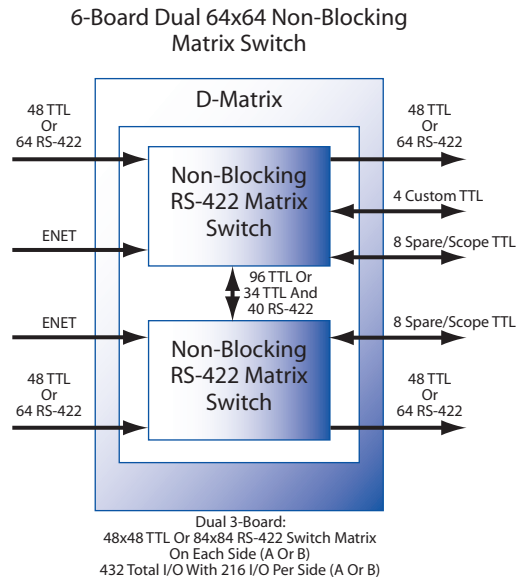


BLOCK DIAGRAM



SPECIFICATIONS

- I/O Connectors
 - TCP/IP
 - 2 Board
 - 4 SCSI 68 Female
 - 32x32 Non-Blocking
 - 4 Custom TTL
 - 16 Scope/Monitor Points
 - ENET Control
 - 4 Board
 - 8 SCSI 68 Female
 - 64x64 Non-Blocking
 - 4 Custom TTL
 - 32 Scope/Monitor Points
 - ENET Control
 - 6 Board
 - 12 SCSI 68 Female
 - Dual 64x64 Non-Blocking
 - Dual ENET Control
- Firmware
 - Xilinx Spartan 3 XC3S1000-FG456-4
- Control
 - Lantronix X Port Using TCP/IP With Full Configuration
- Software Control
 - Optional Telemetry CORBA, Java Graphical User Interface
- Indicators
 - One Configurable LED
 - One FPGA Status Indicator
- Electrical Characteristics
 - Standard TTL 5 Volt Signals 2.0 To 5.5 Logic High, -0.5 To 0.8 Logic Low
 - TIA/EIA-433-B And ITU-T V.11 Compliant Interoperable With Existing 5v RS-422 Networks; Conforms To Military And Industrial Temperature Ranges
- Power
 - 120 VAC
- Data Rates
 - 10 MHz RS-422
 - 1 MHz TTL
- Impedance
 - RS-422 Receiver Impedance 100 Ohms (Optional Impedance Matching)
- Operating Environment
 - 0 °C To 50 °C
 - 10% To 90% Relative Humidity



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